

FIG. 1

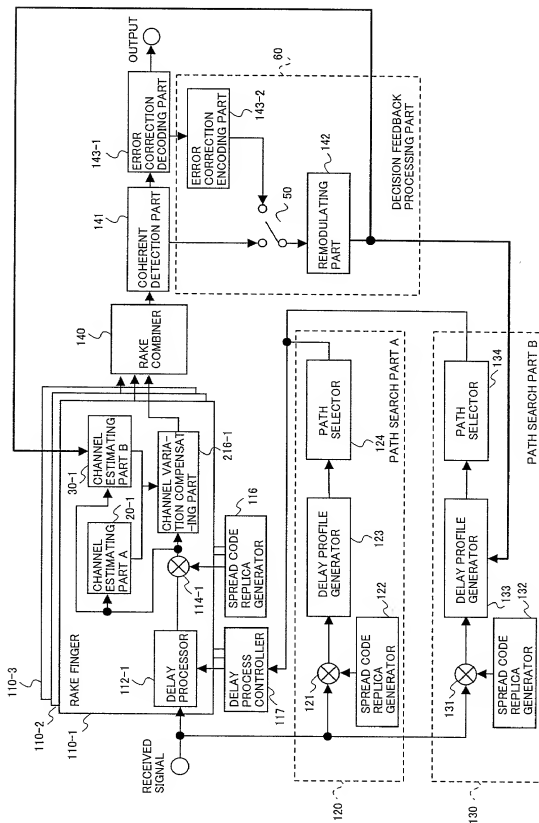
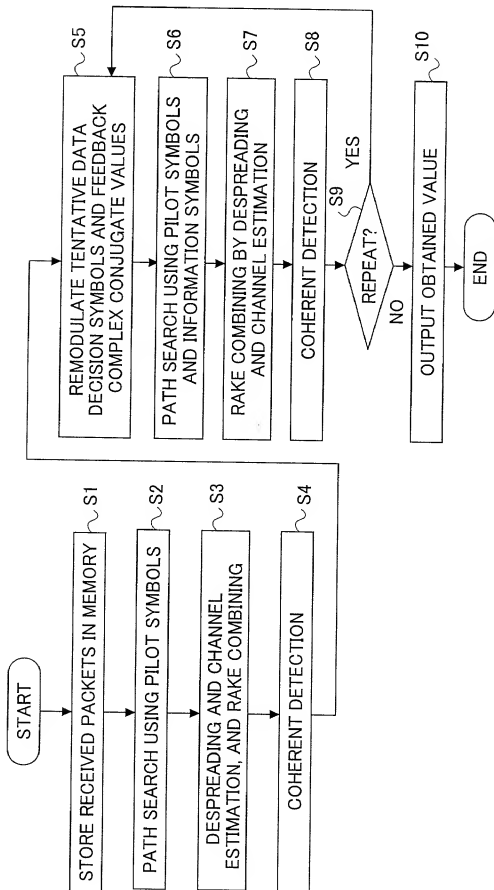
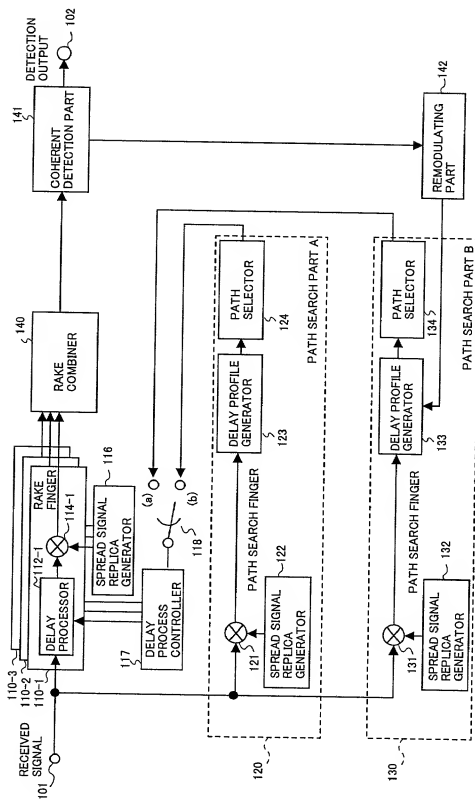


FIG.2



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FIG.3



The diagram illustrates a coherent receiver system. A **RECEIVED SIGNAL** (10) enters a **DELAY PROCESSOR** (110-1, 110-2, 110-3). The output of the delay processor is fed into a **RAKE FINGER** (112-1) and a **SPREAD SIGNAL REPLICATOR** (116). The RAKE FINGER (112-1) is also fed by a **SPREAD SIGNAL REPLICATOR** (114-1). The output of the RAKE FINGER (112-1) is fed into a **RAKE COMBINER** (140). The output of the RAKE COMBINER (140) is fed into a **COHERENT DETECTION PART** (141). The output of the COHERENT DETECTION PART (141) is fed into an **ERROR CORRECTION DECODING PART AND ERROR CORRECTION ENCODING PART** (143). The output of the ERROR CORRECTION DECODING PART AND ERROR CORRECTION ENCODING PART (143) is fed into a **REMODULATING PART** (142). The output of the REMODULATING PART (142) is fed back into the **SPREAD SIGNAL REPLICATOR** (114-1) and the **SPREAD SIGNAL REPLICATOR** (116).

The system also includes two parallel path search parts, **PATH SEARCH PART A** (120) and **PATH SEARCH PART B** (130). Each path search part consists of a **SPREAD SIGNAL REPLICATOR** (122, 132), a **PATH SEARCH FINGER** (121, 131), a **DELAY PROFILE GENERATOR** (123, 133), and a **PATH SELECTOR** (124, 134). The output of the **SPREAD SIGNAL REPLICATOR** (122, 132) is fed into the **PATH SEARCH FINGER** (121, 131). The output of the **PATH SEARCH FINGER** (121, 131) is fed into the **DELAY PROFILE GENERATOR** (123, 133). The output of the **DELAY PROFILE GENERATOR** (123, 133) is fed into the **PATH SELECTOR** (124, 134). The output of the **PATH SELECTOR** (124, 134) is fed into the **RAKE COMBINER** (140). The output of the **PATH SELECTOR** (124, 134) is also fed into the **ERROR CORRECTION DECODING PART AND ERROR CORRECTION ENCODING PART** (143). The output of the **ERROR CORRECTION DECODING PART AND ERROR CORRECTION ENCODING PART** (143) is fed into the **REMODULATING PART** (142). The output of the **REMODULATING PART** (142) is fed back into the **SPREAD SIGNAL REPLICATOR** (114-1) and the **SPREAD SIGNAL REPLICATOR** (116).

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FIG. 5

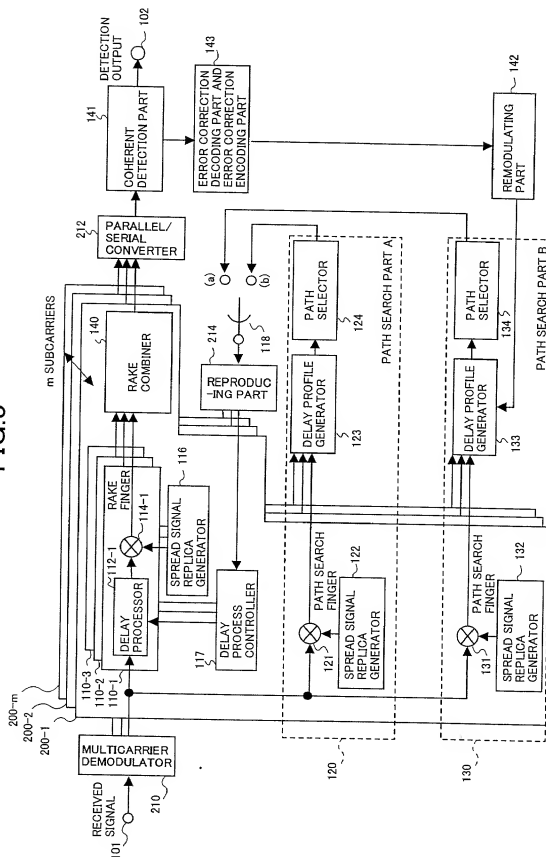
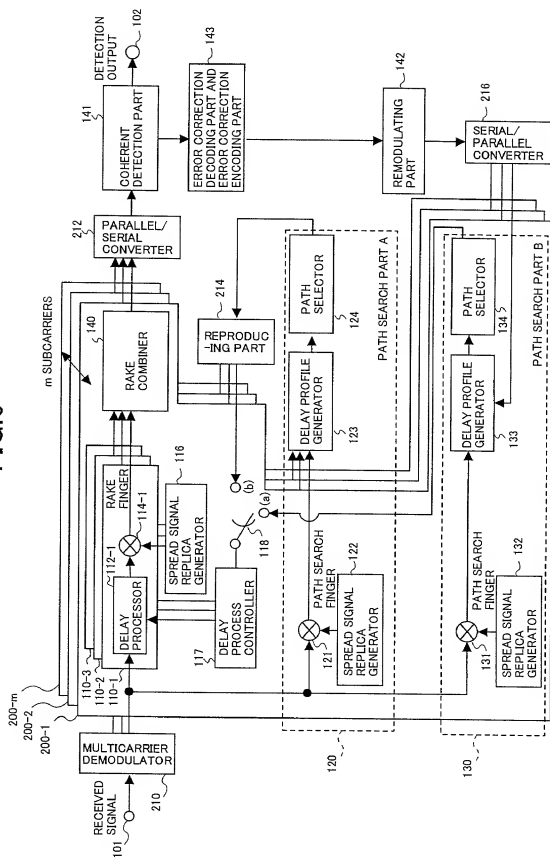
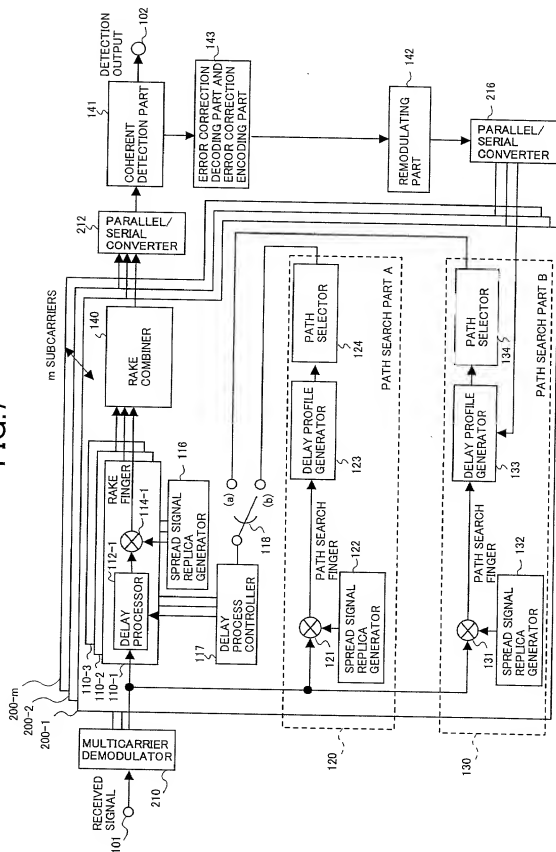


FIG. 6



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FIG. 7



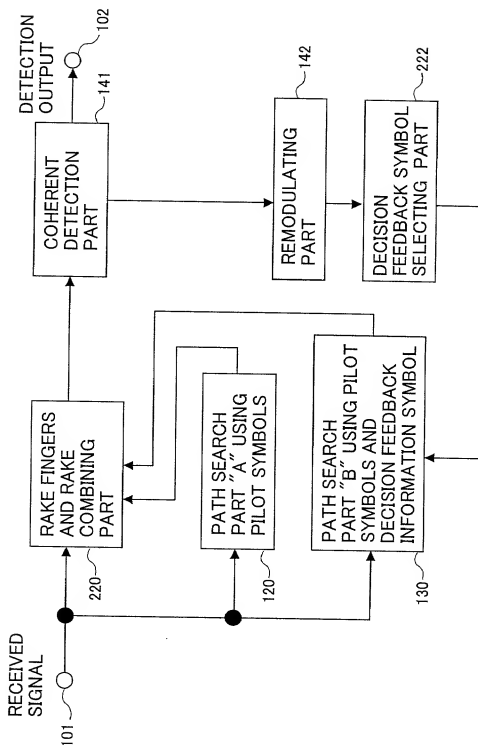


FIG. 8

FIG.9

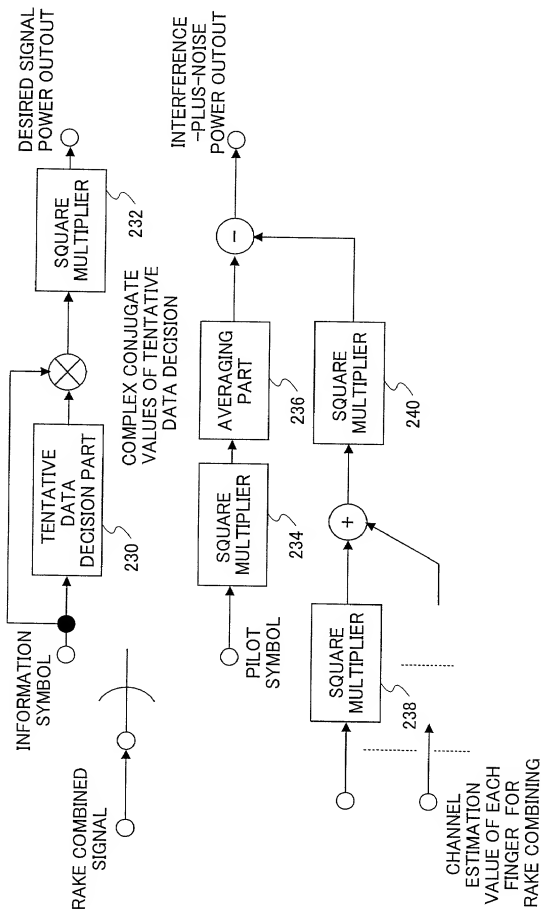


FIG.10

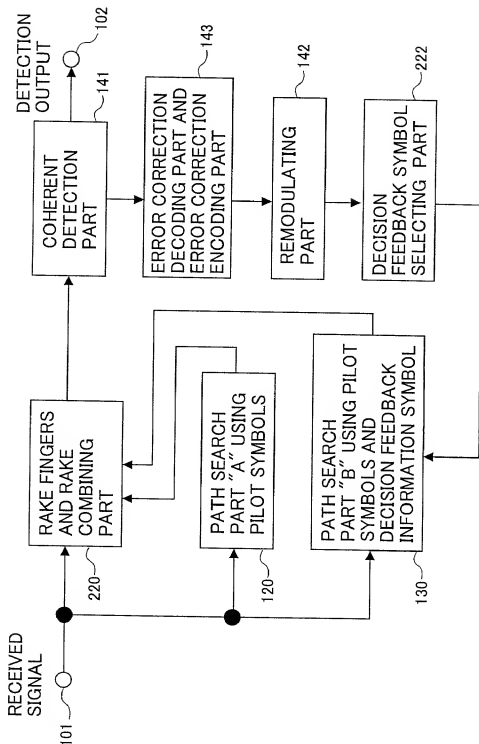


FIG.11

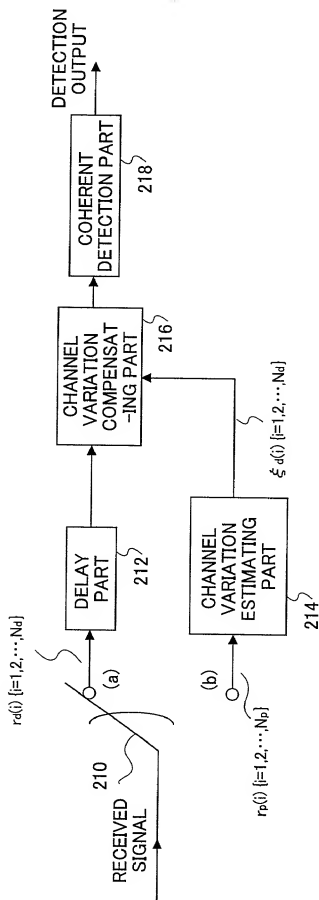


FIG.12

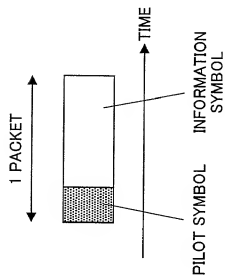
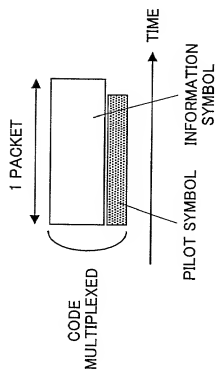


FIG.13



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FIG.14

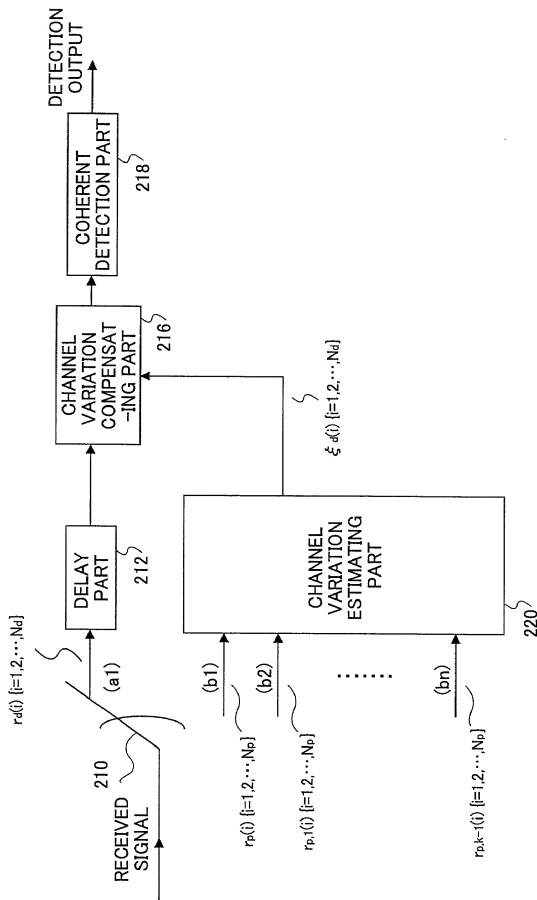


FIG.15

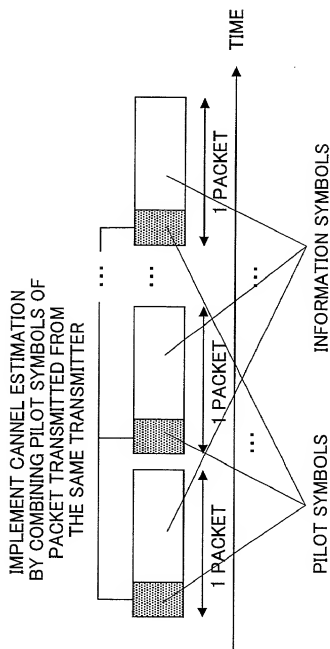
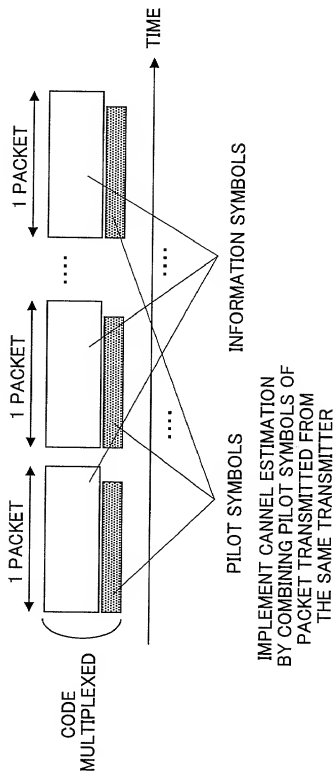
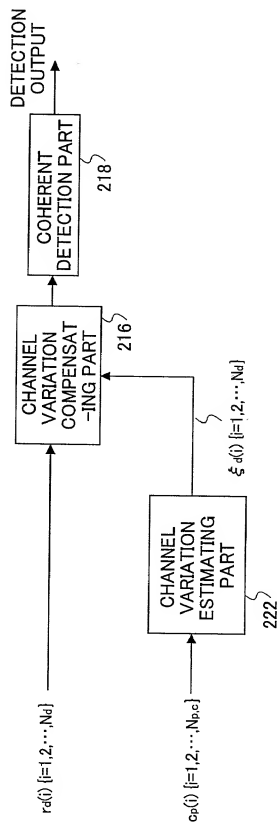


FIG.16



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FIG.17



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FIG.18

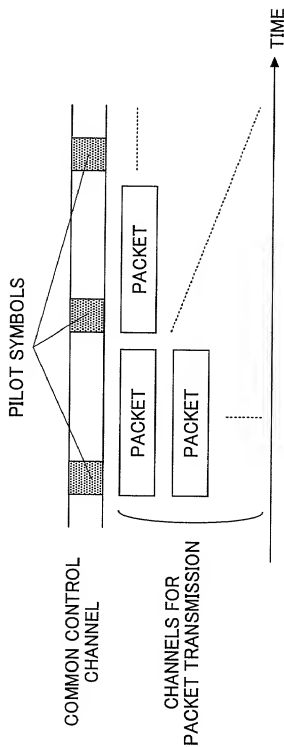


FIG. 19

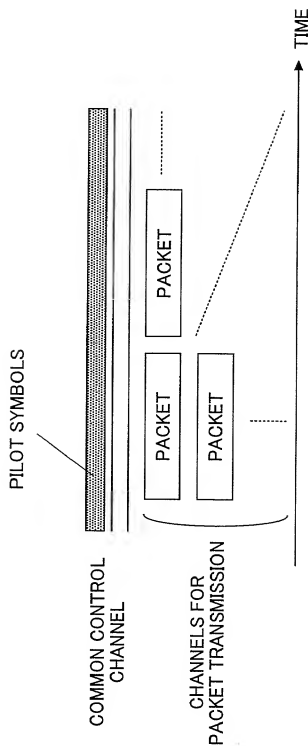
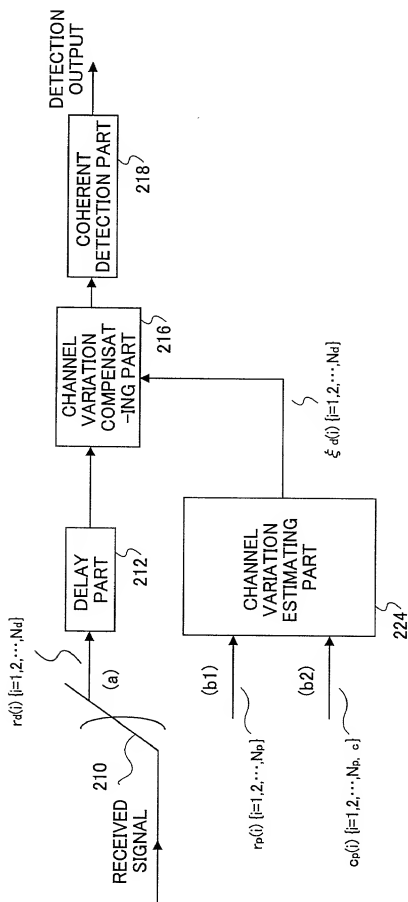


FIG.20



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FIG. 21

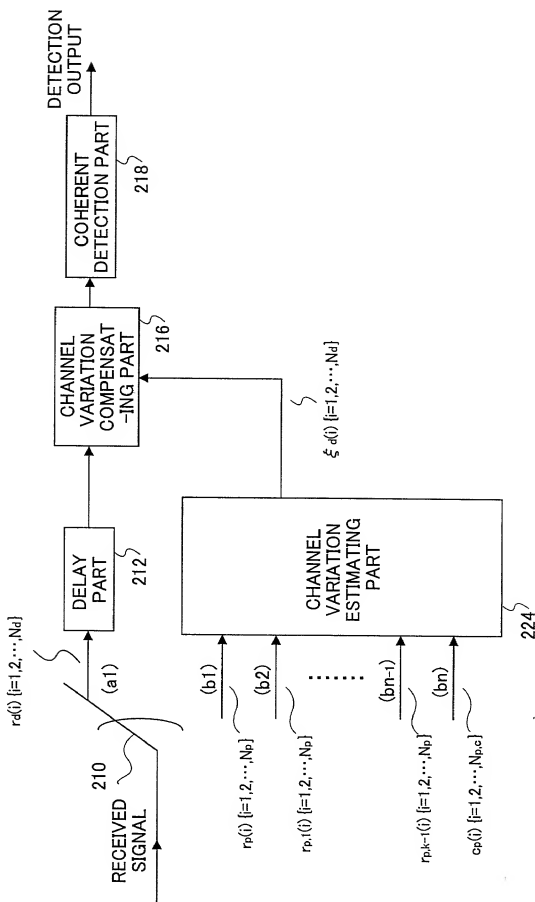
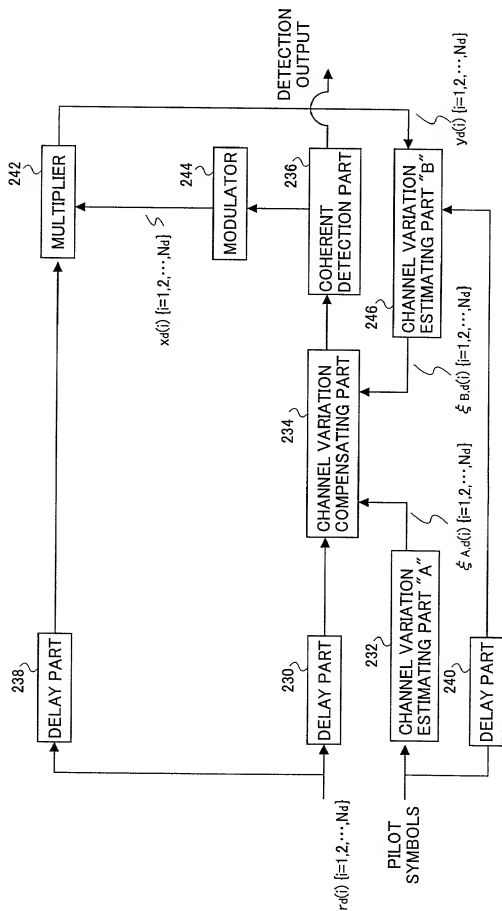
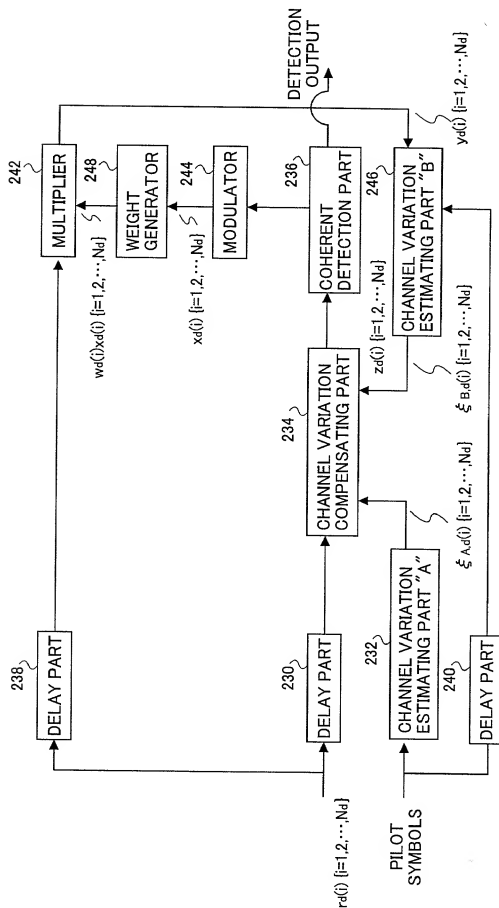


FIG.22



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FIG.23



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FIG.24

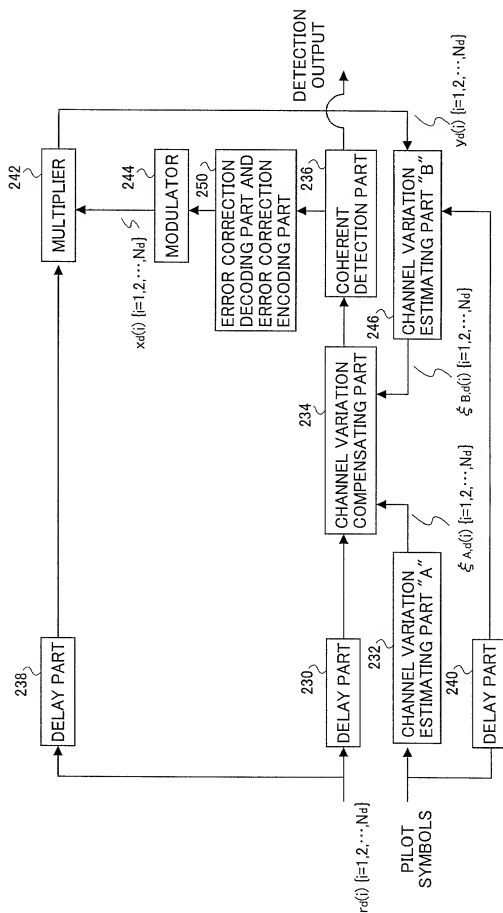


FIG.25

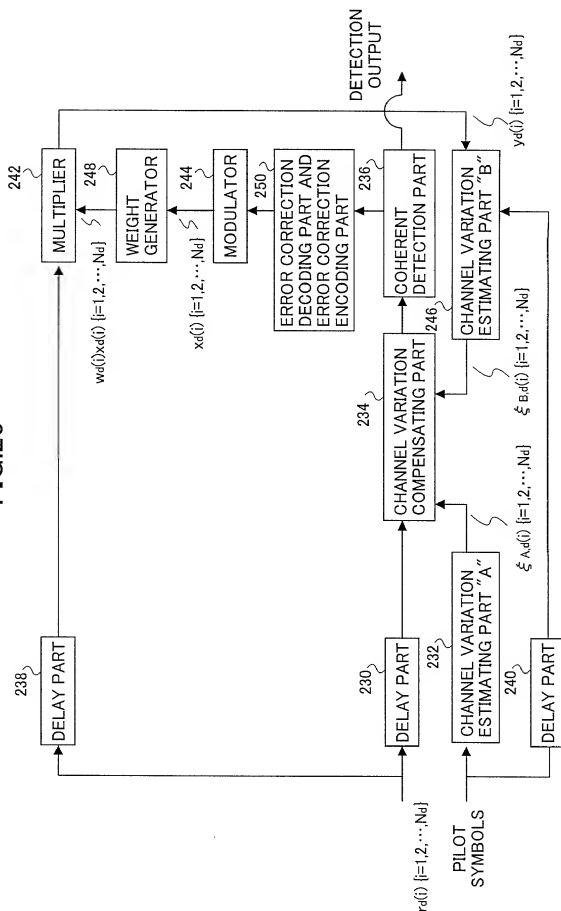


FIG.26

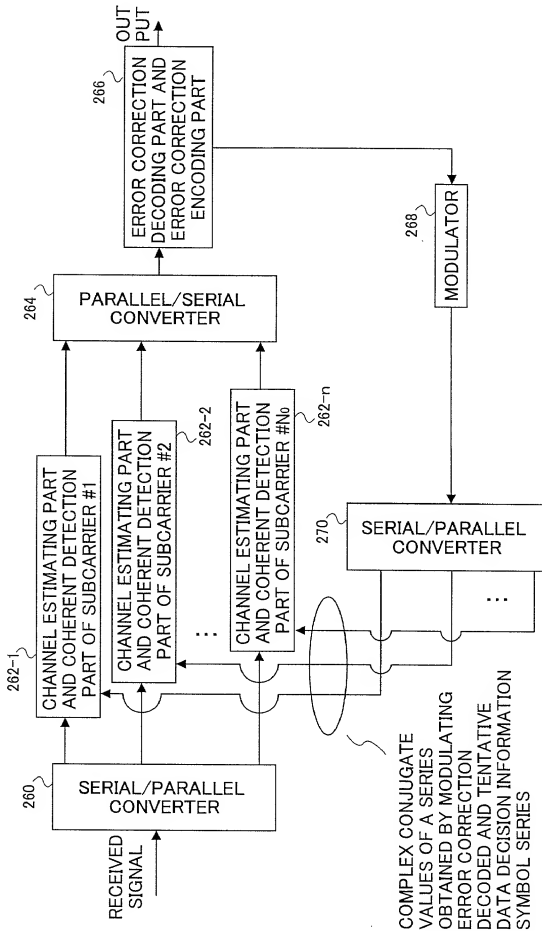


FIG.27

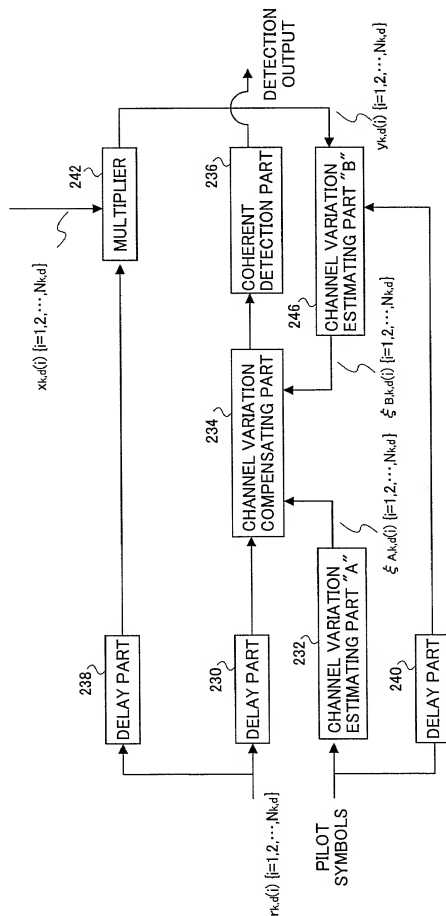
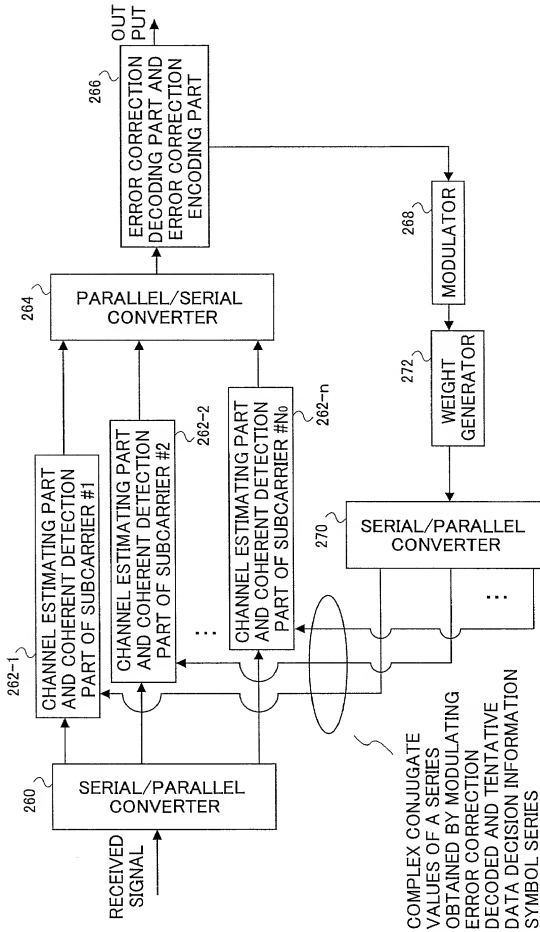


FIG.28



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